FFT PROCEDURES PACKAGE
FOR PROCESSORS WITH ARM CORE

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ABSTRACT  In this article the description of FFT procedures prepared for processors with ARM-9 core is presented. These procedures using the fixed point arithmetic were written in Assembly language to programs written in C language developed. They were prepared for use in the power and quality of energy analysers according to standards: PN-EN 61000-4-7 and PN-EN 61000-4-30. The article also provides the results testing the developed procedures to evaluate their performance and accuracy. The results obtained were compared with similar procedures written directly in C, as well as with practical needs.

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